

ABSTRACT

1 A constant output signal can be achieved by
2 controlling a gain variable amplifier 2 based on a level of
3 an output signal which is derived by variably amplifying a
4 receiving signal S1 (input signal). An AGC output signal
5 S11 is detected by a detector circuit 3, then a detected
6 voltage signal S7 is converted into a digital signal by an
7 A/D converter 4 to output it to a CPU 5. Then, the CPU 5
8 generates a control signal S8 based on the detected voltage
9 signal S7, then an A/D converter 6 converts the control
10 signal S8 into the analogue signal, and then the signal is
11 sent out to the gain variable amplifier 2 to execute gain
12 control. At this time, an RSSI detector portion 9 detects
13 an RSSI signal S10 corresponding to a transmitting signal
14 level from the receiving signal S1, and then the CPU 5
15 sends out the control signal S8 stored at the time of
16 preceding execution of the gain control when the RSSI
17 signal S10 is reduced below a predetermined threshold value
18 S12. In the weak received electric field strength, the
19 control voltage for the gain variable amplifier is
20 prevented reduction, and the proper automatic gain control
21 can be made possible to thus achieve the good receiving
22 characteristics.